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Applicant(s): Andreas Tauber
SEMICONDUCTOR MEMORY APPARATUS WITH VARIABLE
CONTACT CONNECTIONS, AND A CORRESPONDING
SEMICONDUCTOR APPARATUS

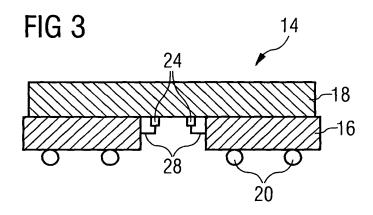
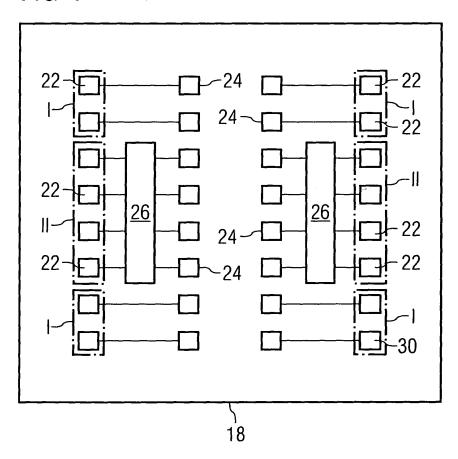
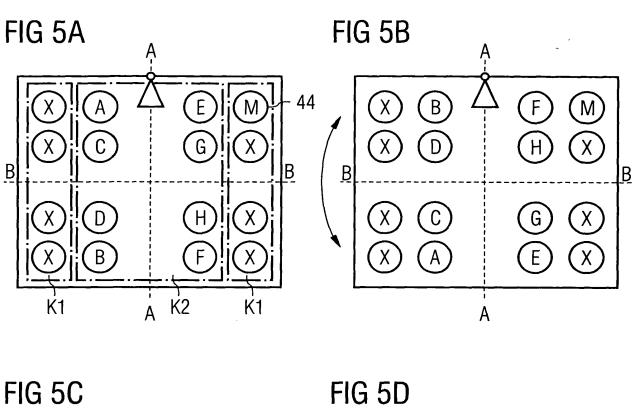


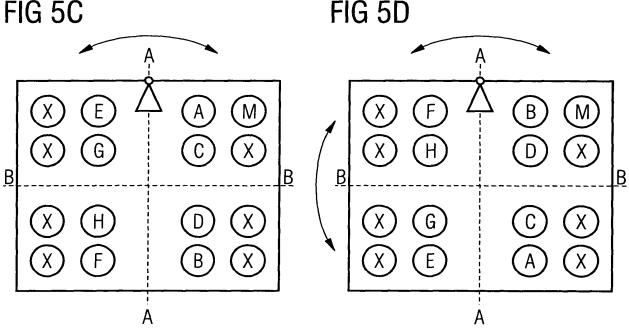
FIG 4



SEMICONDUCTOR MEMORY APPARATUS WITH VARIABLE CONTACT CONNECTIONS, AND A CORRESPONDING







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FIG 6

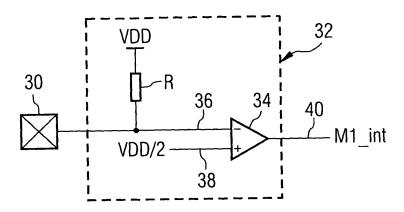
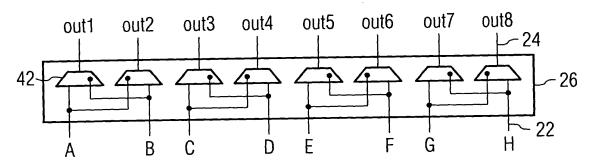


FIG 7A



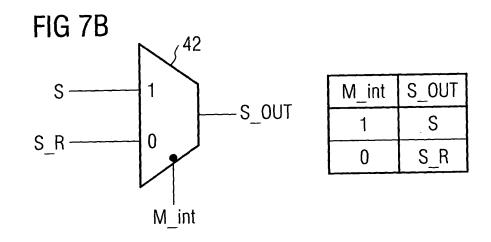
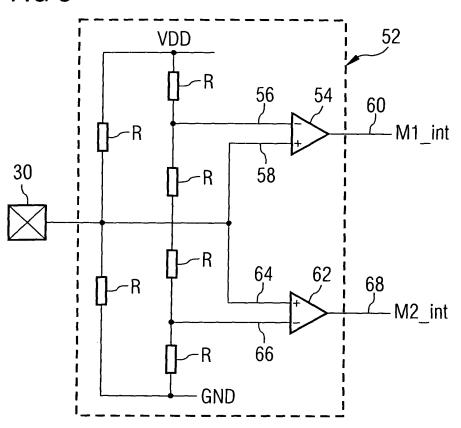


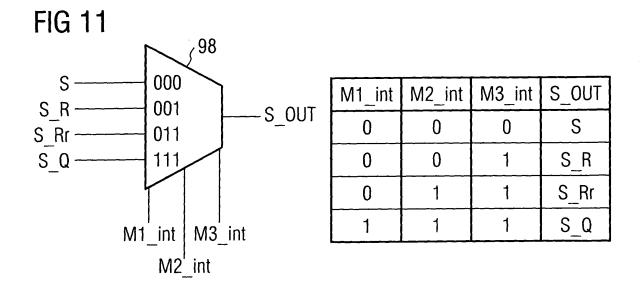
FIG 8



M1_int	M2_int	S_OUT
0	1	S
1	1	S_R
0	0	S_Rr

SEMICONDUCTOR APPARATUS

**FIG 10** 72 LADD **VDD** -R 96 M3\_int -R 94 30 -R 84 88 M2\_int 86 -R 7,6 80 M1 int 78



**GND**